

Sunday 9 October	Monday 10 October	Tuesday 11 October	Wednesday 12 October	Thursday 13 October	Friday 14 October
<p>8:00 - 9:00 Registration for participants to Pre-conference workshops</p> <p>Room EVEREST</p> <p>Room KILIMANDJARO</p> <p>Room MONT BLANC NORD</p> <p>FPGA Labview 1 - 9 to 12:30 Room MONT BLANC SUD</p> <p>Room KILIMANDJARO NORD</p> <p>Room MAKALU</p> <p>Common Data Model - 9 to 17:30</p> <p>Cyber Security - 9 to 12:30 - 14:00 to 17:30</p> <p>JDDD - 9 to 12:30 - 14:00 to 17:30</p> <p>FPGA Labview 2 - 14:00 to 17:30 Room MONT BLANC SUD</p> <p>Tango - 9 to 12:30 - 14:00 to 17:30</p> <p>Open Hardware - 9 to 12:30 - 14:00 to 17:30</p> <p>Registration 16:00 - 18:45</p> <p>Coffee breaks at 10:30 and 15:30 Buffet Lunch at 12:30</p> <p>19:30 Welcome Reception at the Musée</p>	<p>8:00 Registration</p> <p>Opening session 8:30 - 10:00 G. Tonelli , P. Tafforeau</p> <p>Status report 1 10:30 - 12:15</p> <p>Photo session at WTC</p> <p>Project Management 13:30 - 15:00</p> <p>Process Tuning and Feedbacks - 1 - 15:30 - 16:20</p> <p>Poster 1 + Industrial Exhibition 16:30-18:00 Mini Oral A 17:00-17:30 Mini Oral B 17:00-17:30</p> <p>Aperitif Wine & Cheese "French Evening" + Industrial Exhibition 18:00-21:00</p>	<p>Software Technology 1 8:30 - 10:15 Markus Völter</p> <p>Hardware 1 10:45 - 12:30</p> <p>Control System Upgrade 14:00-15:30</p> <p>Status Report 2 16:00-17:30</p> <p>Report from workshops 1,2,3 17:30 - 18:00</p> <p>Apéritif + Industrial Exhibition 18:00-19:30 Round Table with Bruce, Markus and James 19:00 - 20:30</p>	<p>Software Technology 2 8:30 - 10:25 Bruce Eckel</p> <p>Infrastructure Management 10:45-12:15</p> <p>Hardware 2 10:45-12:15</p> <p>Report from workshops 4,5 12:15 - 12:45</p> <p>Poster 2 + Industrial Exhibition 13:30-15:00 Mini Oral C 13:30-14:00 Mini Oral D 13:30-14:00</p> <p>Vizille Castle Visit 15:00 - 19:00</p>	<p>Integration of Industrial Devices 8:30 - 10:15 Dr. Truchard</p> <p>Operation Tools 10:45 -12:30</p> <p>Quality Assurance 10:45-12:30 Miron Livny</p> <p>Data Management 14:00-15:30</p> <p>Embedded + Real Time 1 14:00-15:30</p> <p>Embedded + Real Time 2 16:00 - 17:40</p> <p>Gala evening 19:00 to midnight</p>	<p>Security and safety 8:30 - 10:15 Les Hatton</p> <p>Process Tuning and Feedbacks - 2 10:45-12:15</p> <p>Distributed Computing 10:45-12:30</p> <p>Towards the Future 13:30 - 14:45</p> <p>Closing Session 14:45-15:45</p> <p>Visits of ESRF or Arc-Nucléart 16:00 - 18:15</p>

color code for locations:

- Coffee breaks
- Auditorium
- Atrium
- External
- Makalu